

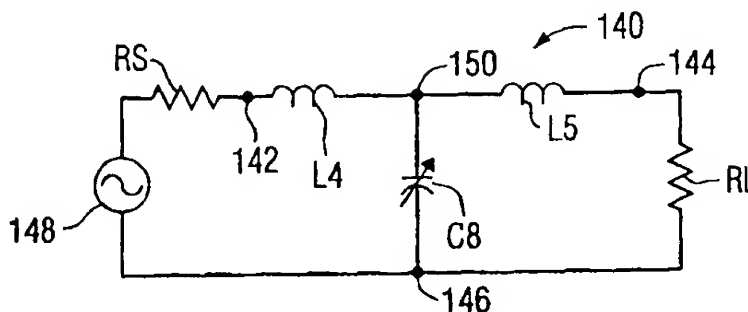


## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<b>(21) International Application Number:</b> PCT/US99/29230 <b>(22) International Filing Date:</b> 9 December 1999 (09.12.99) <b>(30) Priority Data:</b> 60/111,888 11 December 1998 (11.12.98) US <b>(71) Applicant:</b> PARATEK MICROWAVE, INC. [US/US]; 6935-N Oakland Mills Road, Columbia, MD 21045 (US). <b>(72) Inventors:</b> SENGUPTA, Louise, C.; 12 New Haven Boulevard, Warwick, MD 21912 (US). STOWELL, Steven, C.; 9525 Wandering Way, Columbia, MD 21045 (US). ZHU, Yongfei; Suite 6, 5275 Rivendell Lane, Columbia, MD 21044 (US). SENGUPTA, Somnath; 12 New Haven Boulevard, Warwick, MD 21912 (US). CHIU, Luna, H.; 3929 Bush Court, Abingdon, MD 21009 (US). ZHANG, Xubai; Suite 6, 5275 Rivendell Lane, Columbia, MD 21044 (US). <b>(74) Agent:</b> LENART, Robert, P.; Eckert Seamans Cherin & Mellott, LLC, 44th floor, 600 Grant Street, Pittsburgh, PA 15219 (US).		<b>(81) Designated States:</b> AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).  <b>Published</b> <i>With international search report.</i>

**(54) Title:** ELECTRICALLY TUNABLE FILTERS WITH DIELECTRIC VARACTORS**(57) Abstract**

This invention provides a voltage tunable filter comprising an input connection point, an output connection point, and at least one circuit branch electrically coupled to the input connection point and the output connection point and including a voltage tunable dielectric varactor electrically connected to an inductor. The voltage tunable filter can be one of a low-pass, high-pass, band-pass, or band-stop filter. The varactor can include built-in DC blocking capacitors.



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## **ELECTRICALLY TUNABLE FILTERS WITH DIELECTRIC VARACTORS**

### **CROSS REFERENCE TO RELATED PATENT APPLICATION**

5                   **This application claims the benefit of United States Provisional  
Patent Application No. 60/111,888, filed December 11, 1998.**

### **BACKGROUND OF INVENTION**

10                   The present invention relates generally to electronic filters and more  
particularly to filters that include tunable varactors.

                  Electronic filters are widely used in radio frequency (RF) and  
microwave circuits. Tunable filters may significantly improve the performance of the  
circuits, and simplify the circuits. There are two well-known kinds of analog tunable  
filters used in RF applications, one is electrically tuned, usually by diode varactor, and  
15                   the other is mechanically tuned. Mechanically tunable filters have the disadvantages  
of large size, low speed, and heavy weight. Diode-tuned filters that include  
conventional semiconductor varactor diodes suffer from low power handling capacity,  
that is limited by intermodulation of the varactor, which causes signals to be  
generated at frequencies other than those desired. This intermodulation is caused by  
20                   the highly non-linear response of conventional semiconductor varactors to voltage  
control.

Tunable filters for use in radio frequency circuits are well known. Examples of such filters can be found in United States Patents No. 5,917,387, 5,908,811, 5,877,123, 5,869,429, 5,752,179, 5,496,795 and 5,376,907.

Varactors can be used as tunable capacitors in tunable filters. Common varactors used today are Silicon and GaAs based diodes. The performance of these varactors is defined by the capacitance ratio,  $C_{\max}/C_{\min}$ , frequency range and figure of merit, or Q factor ( $1 / \tan \delta$ ) at the specified frequency range. The Q factors for these semiconductor varactors for frequencies up to 2 GHz are usually very good. However, at frequencies above 2 GHz, the Q factors of these varactors degrade rapidly. At 10 GHz the Q factors for these varactors are usually only about 30.

Varactors that utilize a thin film ferroelectric ceramic as a voltage tunable element in combination with a superconducting element have been described. For example, United States Patent No. 5,640,042 discloses a thin film ferroelectric varactor having a carrier substrate layer, a high temperature superconducting layer deposited on the substrate, a thin film dielectric deposited on the metallic layer, and a plurality of metallic conductive means disposed on the thin film dielectric, which are placed in electrical contact with RF transmission lines in tuning devices. Another tunable capacitor using a ferroelectric element in combination with a superconducting element is disclosed in United States Patent No. 5,721,194.

Commonly owned United States Patent Application Serial No. 09/149,126, filed October 15, 1999, and titled "Voltage Tunable Varactors And Tunable Devices Including Such Varactors", discloses voltage tunable varactors that operate at room temperature and various devices that include such varactors. Commonly owned United States Patent Application Serial No. 09/434,433, filed November 4, 1999, and titled "Ferroelectric Varactor With Built-In DC Blocks" discloses voltage tunable varactors that include built-in DC blocking capacitors. These varactors operate at room temperatures to provide a tunable capacitance.

There is a need for tunable filters that can operate at radio frequencies with reduced intermodulation products and at temperatures above those necessary for superconduction.

## SUMMARY OF INVENTION

This invention provides a voltage tunable filter comprising an input connection, an output connection, and a circuit branch electrically coupled to the input connection and the output connection and including a voltage tunable dielectric varactor electrically connected to an inductor. The voltage tunable filter can be one of a low-pass, high-pass, band-pass, or band-stop filter. The varactor can include built-in DC blocking capacitors.

In the preferred embodiment, the voltage tunable dielectric varactor includes a substrate having a first dielectric constant and having a generally planar surface, a tunable dielectric layer positioned on the generally planar surface of the substrate, with the tunable dielectric layer having a second dielectric constant greater than the first dielectric constant, and first and second electrodes positioned on a surface of the tunable dielectric layer opposite the generally planar surface of the substrate. The first and second electrodes are separated to form a gap therebetween. A bias voltage applied to the electrodes changes the capacitance of the varactor between an input and an output thereof.

The present invention provides radio frequency (RF) electrically tunable filters, tuned by dielectric voltage-variable capacitors. The filters can handle high power with lower intermodulation distortion.

## BRIEF DESCRIPTION OF THE DRAWINGS

A full understanding of the invention can be gained from the following description of the preferred embodiments when read in conjunction with the accompanying drawings in which:

FIG. 1 is a top plan view of a planar voltage tunable varactor as described in United States Patent Application Serial No. 09/149,126 that can be used in the preferred embodiment of the present invention;

FIG. 2 is a cross-sectional view of the varactor of FIG. 1, taken along line 2-2;

FIGs. 3a, 3b and 3c are graphs illustrating the capacitance and loss tangent of voltage tunable varactors constructed in accordance with this invention at various operating frequencies and gap widths;

FIG. 4 is a top view of a planar varactor assembly with built-in DC blocking capacitors as described in United States Patent Application Serial No. 09/434,433;

FIG. 5 is a cross sectional view of the varactor assembly of FIG. 4,  
5 taken along line 5-5;

FIG. 6 is a schematic diagram of the varactor of FIGs. 4 and 5;

FIG. 7 is a schematic diagram of an example Chebyshev bass-pass filter constructed in accordance with this invention;

FIG. 8 is a graph of the attenuation of the filter shown in FIG. 7  
10 operated at various bias voltages on the varactors;

FIG. 9 is a schematic diagram of a low pass filter constructed in accordance with this invention;

FIG. 10 is a graph of the losses of the filter shown in FIG. 9 operated at various bias voltages on the varactor;

FIG. 11 is a schematic diagram of a high pass filter constructed in  
15 accordance with this invention;

FIG. 12 is a graph of the losses of the filter shown in FIG. 11 operated at various bias voltages on the varactors;

FIG. 13 is a schematic diagram of a band stop filter constructed in  
20 accordance with this invention; and

FIG. 14 is a graph of the losses of the filter shown in FIG. 13 operated at various bias voltages on the varactors.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring to the drawings, FIGs. 1 and 2 are top and cross sectional  
25 views of a varactor 10 as described in the above-mentioned United States Patent Application No. 09/149,126. The varactor 10 includes a substrate 12 having a generally planar top surface 14. A tunable dielectric layer 16 is positioned adjacent to the top surface of the substrate. A pair of metal electrodes 18 and 20 is positioned on top of the dielectric layer. The substrate 12 is comprised of a material having a  
30 relatively low permittivity such as MgO, Alumina,  $\text{LaAlO}_3$ , Sapphire, or a ceramic. For the purposes of this invention, a low permittivity is a permittivity of less than

about 30. The tunable dielectric layer 16 is comprised of a material having a permittivity in a range from about 20 to about 2000, and having a tunability in the range from about 10% to about 80% at a bias voltage of about 10 V/ $\mu$ m. In the preferred embodiment this layer is preferably comprised of Barium-Strontium Titanate,  $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$  (BSTO), where x can range from zero to one, or BSTO-composite ceramics. Examples of such BSTO composites include, but are not limited to: BSTO-MgO, BSTO-MgAl<sub>2</sub>O<sub>4</sub>, BSTO-CaTiO<sub>3</sub>, BSTO-MgTiO<sub>3</sub>, BSTO-MgSrZrTiO<sub>6</sub>, and combinations thereof. The tunable layer in one preferred embodiment has a dielectric permittivity greater than 100 when subjected to typical DC bias voltages, for example, voltages ranging from about 5 volts to about 300 volts. A gap 22 of width g, is formed between the electrodes 18 and 20. The gap width must be optimized to increase ratio of the maximum capacitance  $C_{\text{max}}$  to the minimum capacitance  $C_{\text{min}}$  ( $C_{\text{max}}/C_{\text{min}}$ ) and increase the quality factor (Q) of the device. The width of this gap has the most influence on the varactor parameters. The optimal width, g, will be determined by the width at which the device has maximum  $C_{\text{max}}/C_{\text{min}}$  and minimal loss tangent.

A controllable voltage source 24 is connected by lines 26 and 28 to electrodes 18 and 20. This voltage source is used to supply a DC bias voltage to the dielectric layer, thereby controlling the permittivity of the layer. The varactor also includes an RF input 30 and an RF output 32. The RF input and output are connected to electrodes 18 and 20, respectively, by soldered or bonded connections.

The varactors may use gap widths of less than 5-50  $\mu$ m. The thickness of the dielectric layer ranges from about 0.1  $\mu$ m to about 20  $\mu$ m. A sealant 34 is positioned within the gap and can be any non-conducting material with a high dielectric breakdown strength to allow the application of high voltage without arcing across the gap. In the preferred embodiment, the sealant can be epoxy or polyurethane.

The length of the gap L can be adjusted by changing the length of the ends 36 and 38 of the electrodes. Variations in the length have a strong effect on the capacitance of the varactor. The gap length will be optimized for this parameter. Once the gap width has been selected, the capacitance becomes a linear function of the

length  $L$ . For a desired capacitance, the length  $L$  can be determined experimentally, or through computer simulation.

The thickness of the tunable dielectric layer also has a strong effect on the  $C_{\max}/C_{\min}$ . The optimum thickness of the ferroelectric layers will be determined by the thickness at which the maximum  $C_{\max}/C_{\min}$  occurs. The ferroelectric layer of the varactor of FIGs. 1 and 2 can be comprised of a thin film, thick film, or bulk dielectric material such as Barium-Strontium Titanate,  $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$  (BSTO), BSTO and various oxides, or a BSTO composite with various dopant materials added. All of these materials exhibit a low loss tangent. For the purposes of this description, for operation at frequencies ranging from about 1.0 GHz to about 10 GHz, the loss tangent would range from about 0.0001 to about 0.001. For operation at frequencies ranging from about 10 GHz to about 20 GHz, the loss tangent would range from about 0.001 to about 0.01. For operation at frequencies ranging from about 20 GHz to about 30 GHz, the loss tangent would range from about 0.005 to about 0.02.

The electrodes may be fabricated in any geometry or shape containing a gap of predetermined width. The required current for manipulation of the capacitance of the varactors disclosed in this invention is typically less than 1  $\mu\text{A}$ . In the preferred embodiment, the electrode material is gold. However, other conductors such as copper, silver or aluminum, may also be used. Gold is resistant to corrosion and can be readily bonded to the RF input and output. Copper provides high conductivity, and would typically be coated with gold for bonding or nickel for soldering. The varactors of FIGs. 1 and 2 can be fabricated using bulk, thick film, and thin film techniques.

FIGs. 1 and 2 show a voltage tunable planar varactor having a planar electrode with a predetermined gap distance on a single layer tunable bulk, thick film or thin film dielectric. The applied voltage produces an electric field across the gap of the tunable dielectric that produces an overall change in the capacitance of the varactor. The width of the gap can range from 5 to 50  $\mu\text{m}$  depending on the performance requirements.

Such varactors operate at room temperature and can have  $Q$  factors ranging from about 50 to about 10,000 when operated at frequencies ranging from



about 1 GHz to about 40 GHz. The capacitance (in pF) and the loss factor ( $\tan \delta$ ) of the varactors measured at 3, 10 and 20 GHz for gap distances of 10 and 20  $\mu\text{m}$  are shown in FIGs. 3a, 3b and 3c. Based on the data shown in FIGs. 3a, 3b and 3c, the Q's for the varactors are approximately the following: 200 at 3 GHz, 80 at 10 GHz, 45-55 at 20 GHz. In comparison, typical Q's for GaAs semiconductor diode varactors are as follows: 175 at 2 GHz, 35 at 10 GHz and much less at even higher frequency. Therefore at frequencies greater than or equal to 10 GHz the varactors of this invention have much better Q factors.

Referring to the drawings, FIGs. 4 and 5 are top and cross sectional views of a varactor assembly 40 having built in DC blocking capacitors as described in United States Patent Application Serial No. 09/434,433. The varactor assembly 40 includes a substrate 42 having a generally planar top surface 44. A tunable dielectric layer 46 is positioned adjacent to the top surface of the substrate. Metal electrodes 48 and 50 are positioned on top of the dielectric layer. The electrodes 48 and 50 are shaped to have projections 52 and 54. The ends of these projections form a gap 56 on the surface of the tunable dielectric layer. The combination of electrodes 48 and 50, and tunable dielectric layer 46 form a tunable capacitor 84. The capacitance of the tunable capacitor can be changed by applying a bias voltage to the electrodes 48 and 50.

In the preferred embodiment, the substrate 42 is comprised of a material having a relatively low permittivity such as MgO, Alumina,  $\text{LaAlO}_3$ , Sapphire, or a ceramic. For the purposes of this description, a low permittivity is a permittivity of less than about 30. In the preferred embodiment, the tunable dielectric layer 16 is comprised of a material having a permittivity in a range from about 20 to about 2000, and having a tunability in the range from about 10% to about 80% at a bias voltage of about 10 V/ $\mu\text{m}$ . The tunable dielectric layer can be comprised of Barium-Strontium Titanate,  $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$  (BSTO), where x can range from zero to one, or BSTO-composite ceramics. Examples of such BSTO composites include, but are not limited to: BSTO-MgO, BSTO-MgAl<sub>2</sub>O<sub>4</sub>, BSTO-CaTiO<sub>3</sub>, BSTO-MgTiO<sub>3</sub>, BSTO-MgSrZrTiO<sub>6</sub>, and combinations thereof. The dielectric film of the dielectric capacitor may be deposited by screen printer, laser ablation, metal-organic solution

deposition, sputtering, or chemical vapor deposition techniques. The tunable layer in one preferred embodiment has a dielectric permittivity greater than 100 when subjected to typical DC bias voltages, for example, voltages ranging from about 5 volts to about 300 volts. The gap width must be optimized to increase ratio of the maximum capacitance  $C_{\max}$  to the minimum capacitance  $C_{\min}$  ( $C_{\max}/C_{\min}$ ) and increase the quality factor (Q) of the device. The width of this gap has the most influence on the varactor parameters. The optimal width, g, will be determined by the width at which the device has maximum  $C_{\max}/C_{\min}$  and minimal loss tangent.

A controllable voltage source 58 is connected by lines 60 and 62 to electrodes 48 and 50. This voltage source is used to supply a DC bias voltage to the dielectric layer, thereby controlling the permittivity of the layer. The varactor assembly further includes first and second non-tunable dielectric layers 64 and 66 positioned adjacent to the generally planar surface of the substrate 42 and on opposite sides of the tunable dielectric layer 46. Electrode 48 extends over a portion of the top surface of non-tunable material 64. Electrode 68 is positioned adjacent a top surface of non-tunable layer 64 such that a gap 70 is formed between electrodes 48 and 68. The combination of electrodes 48 and 68 and non-tunable layer 64 forms a first DC blocking capacitor 72. The varactor assembly also includes an RF input 80 and an RF output 82.

Electrode 74 is positioned adjacent a top surface of non-tunable layer 66 such that a gap 76 is formed between electrodes 50 and 74. The combination of electrodes 50 and 74 and non-tunable layer 66 forms a second DC blocking capacitor 78. The dielectric films of the DC blocking capacitors may be deposited by screen printer, laser ablation, metal-organic solution deposition, sputtering, or chemical vapor deposition techniques.

An RF input 80 is connected to electrode 68. An RF output 82 is connected to electrode 74. The RF input and output are connected to electrodes 68 and 74, respectively, by soldered or bonded connections. The non-tunable dielectric layers 64 and 66, in the DC blocking capacitors 72 and 78, are comprised of a high dielectric constant material, such as a BSTO composite. The DC blocking capacitors 72 and 78 are electrically connected in series with the tunable capacitor 84 to isolate

the DC bias from the outside of the varactor assembly 40. To increase the capacitance of the two DC blocking capacitors 72 and 78 the electrodes have an interdigital arrangement as shown in FIG. 4. FIG. 6 is a schematic diagram of the varactor of FIGs. 4 and 5 showing the three capacitors formed by the structure.

5 In the preferred embodiments, the varactors may use gap widths of 5-50  $\mu\text{m}$ . The thickness of the tunable dielectric layer ranges from about 0.1  $\mu\text{m}$  to about 20  $\mu\text{m}$ . A sealant can be inserted into the gaps to increase breakdown voltage. The sealant can be any non-conducting material with a high dielectric breakdown strength to allow the application of high voltage without arcing across the gap, for  
10 example, epoxy or polyurethane.

This invention utilizes room temperature tunable dielectric the varactors such as those shown in FIGs. 1, 2, 4 and 5 in an electronically tunable RF filter. The lump element filter in the present invention may be low-pass, high-pass, band-pass, or band-stop designed by Bessel, Butterworth, Chebyshev, Elliptical or  
15 other methods.

FIG. 7 is a schematic diagram of a band pass filter 100 constructed in accordance with this invention. This capacitor coupled LC filter circuit, which is commonly referred to as a capacitively coupled tank circuit, includes a plurality of resonators 102, 104 and 106 with capacitive coupling between those resonators, input  
20 connection points 108 and 110, and output connection points 112 and 114. In particular, resonator 102, comprising the parallel connection of capacitor C2 and inductor L1, is connected to node 116 and input connection point 110 and output connection point 114, and is coupled to input connection point 108 through capacitor C1. Similarly, resonator 104, comprising the parallel connection of capacitor C4 and  
25 inductor L2, is connected to node 118 and input connection point 110 and output connection point 114, and is coupled to node 116 through capacitor C3. Resonator 106, comprising the parallel connection of capacitor C6 and inductor L3, is connected to node 120 and input connection point 110 and output connection point 114, and is coupled to node 118 through capacitor C5. In addition, resonator 106 is coupled to  
30 output connection point 112 through capacitor C7. The filter is tuned by varactors C2, C4 and C6, which in the preferred embodiment are constructed in accordance

with either FIGs. 1 and 2 or FIGs. 4 and 5. Common connection points 110 and 114 may be connected to ground.

In the preferred embodiment, with zero bias voltage on the tunable capacitors, capacitors C1 and C7 are 5.6 pF, capacitors C3 and C5 are 0.48 pF, capacitors C2 and C6 are 8.0 pF, capacitor C4 is 13.1 pF, and inductors L1, L2 and L3 are 500 nH. The input and output of the filter are matched to 50  $\Omega$ . FIG. 8 is a graph of the attenuation of the filter shown in FIG. 7 wherein capacitors C2, C4 and C6 are voltage tunable varactors operated at various bias voltages. Curves 124, 126, 128, 130 and 132 shown the filter attenuation at the bias voltages shown in Table I.

Table I. Varactor bias voltages.

Curve	C2 Bias	C4 Bias	C6 Bias
124	0	0	0
126	180	80	180
128	400	160	400
130	600	210	600
132	700	500	700

FIG. 9 is a schematic diagram of a low pass filter constructed in accordance with this invention. In FIG. 9, low pass filter 140 includes an input connection point 142, and output connection point 144 and a common connection point 146. An RF source 148 supplies an RF signal to the filter. Resistor RS represents the filter input impedance. A load as represented by resistor RL is connected between the output connection point 144 and the common connection point 146. Inductors L4 and L5 are electrically connected in series between input connection point 142 and output connection point 144. A tunable varactor as represented by capacitor C8 is connected between the common connection point 146 and a node 150 between inductors L4 and L5.

FIG. 10 is a graph of the losses of the filter shown in FIG. 9 operated at various bias voltages on the varactor. In the embodiment used to construct the graph of FIG. 10, RS = RL = 50  $\Omega$ , L4 = L5 = 217 nH, and C8 = 133.8 pF at zero bias.

Curves 156 and 152 represent the insertion loss and return loss at zero bias voltage, respectively. Curves 158 and 154 represent the insertion loss and return loss at 500 volts bias, respectively.

FIG. 11 is a schematic diagram of a high pass filter constructed in accordance with this invention. In FIG. 11, high pass filter 160 includes an input connection point 162, and output connection point 164 and a common connection point 166. An RF source 168 supplies an RF signal to the filter. Resistor RS represents the input impedance of the filter. A load as represented by resistor RL is connected between the output connection point 164 and the common connection point 166. Tunable varactors as represented by capacitors C9 and C10 are electrically connected in series between input connection point 162 and output connection point 164. Inductor L6 is connected between the common connection point 166 and a node 170 between capacitors C9 and C10.

FIG. 12 is a graph of the losses of the filter shown in FIG. 11 operated at various bias voltages on the varactor. In the embodiment used to construct the graph of FIG. 10,  $RS = RL = 50 \Omega$ ,  $L6 = 52.6 \text{ nH}$ , and  $C9 = C10 = 32.4 \text{ pF}$  at zero bias. Curves 172 and 176 represent the insertion loss and return loss at zero bias voltage, respectively. Curves 174 and 178 represent the insertion loss and return loss at 600 volts bias, respectively.

FIG. 13 is a schematic diagram of a band stop filter 180 constructed in accordance with this invention. In FIG. 13, band stop filter 180 includes an input connection point 182, and output connection point 184 and a common connection point 186. An RF source 188 supplies an RF signal to the filter. Resistor RS represents the input impedance of the filter. A load as represented by resistor RL is connected between the output connection point 184 and the common connection point 186. A first circuit branch 192 comprising the parallel connection of inductor L7 and a varactor represented by capacitor C11 is electrically connected between input connection point 182 and node 190. A second circuit branch 194 comprising the parallel connection of inductor L8 and a varactor represented by capacitor C12 is electrically connected between output connection point 184 and node 190. A third circuit branch 196 comprising the series connection of inductor L9 and a varactor

represented by capacitor C13 is electrically connected between common connection point 186 and node 190.

FIG. 14 is a graph of the losses of the filter shown in FIG. 13 operated at various bias voltages on the varactors. In the embodiment used to construct the graph of FIG. 13,  $R_S = R_L = 50 \Omega$ ,  $L_7 = L_8 = 7.83 \text{ nH}$ ,  $L_9 = 1457 \text{ nH}$ ,  $C_{11} = C_{12} = 899 \text{ pF}$  at zero bias voltage, and  $C_{13} = 4.83 \text{ pF}$  at zero bias. Curves 198 and 202 represent the insertion loss and return loss at zero bias voltage, respectively. Curves 200 and 204 represent the insertion loss and return loss at 500 volts bias, respectively.

The lumped element filter in the present invention may be designed by Bessel, Butterworth, Chebyshev, Elliptical or other methods. Examples of band-pass, low pass, high pass and band stop filters have been presented. Dielectric varactors with built-in DC blocks can be used in the filter as the tunable elements. By utilizing low loss ( $\tan \delta < 0.02$ ) dielectrics of predetermined dimensions, the varactors of FIGs. 1, 2, 4 and 5 can operate at high levels at high frequencies, for example, greater than 3 GHz.

The dielectric varactors of Figs. 1, 2, 4 and 5 operate at high speeds, with high Quality Factor (Q), high power handling, and more importantly low intermodulation distortion products. Filters using dielectric varactors have better performance than semiconductor diode-tuned filters, especially in the properties of high power handling, low intermodulation distortion, and the ability to cover capacitance ranges that are not possible with conventional varactors.

In the preferred embodiment, varactors using dielectric materials can work at much higher capacitance values than conventional diode varactors. This allows the construction of compact electronically tunable filters using lumped element capacitors with performances that are not possible with conventional varactors. A low loss, highly tunable dielectric varactor with or without built-in DC blocks may be used in the present invention, the built-in DC block dielectric varactor may reduce DC block insertion loss, and make it easier to use in the filter design. In addition, the tunable dielectric varactors of this invention have increased RF power handling capability and reduced power consumption and cost.

Accordingly the present invention, by utilizing dielectric varactors, provides high performance electrically tunable filters that operate in the RF frequency range. This invention has many practical applications and many other modifications of the disclosed devices may be obvious to those skilled in the art without departing  
5 from the spirit and scope of this invention. While the present invention has been described in terms of what are at present its preferred embodiments, various modifications of such embodiments can be made without departing from the scope of the invention, which is defined by the claims.

## WHAT IS CLAIMED IS:

1. A tunable filter comprising:  
an input connection point;  
an output connection point; and  
5 a first circuit branch electrically coupled to said input connection point  
and said output connection point and including voltage tunable dielectric varactor  
electrically connected to an inductor.
2. A tunable filter in accordance with claim 1, wherein the filter  
comprises one of a low-pass, high-pass, band-pass, or band-stop filter.
- 10 3. A tunable filter in accordance with claim 1, wherein said  
varactor includes built-in DC blocking capacitors.
4. A tunable filter in accordance with claim 1, wherein the filter  
comprises one of a Bessel, Butterworth, Chebyshev or elliptical filter.
5. A tunable filter in accordance with claim 1, wherein said  
15 voltage tunable dielectric varactor comprises:  
a substrate having a first dielectric constant and having generally a  
planar surface; a tunable dielectric layer positioned on the generally planar surface of  
the substrate, the tunable dielectric layer having a second dielectric constant greater  
than said first dielectric constant; and first and second electrodes positioned on a  
20 surface of the tunable dielectric layer opposite the generally planar surface of the  
substrate, said first and second electrodes being separated to form a gap therebetween.
6. A tunable filter in accordance with claim 5, wherein said  
voltage tunable dielectric varactor further comprises:  
an insulating material in said gap.



7. A tunable filter in accordance with claim 5, wherein the tunable dielectric layer has a permittivity greater than about 100.

8. A tunable filter in accordance with claim 5, wherein the substrate has a permittivity of less than about 30.

5 9. A tunable filter in accordance with claim 5, wherein the tunable dielectric layer has a permittivity in a range from about 20 to about 2000, and a tunability in a range from about 10% to about 80% at a bias voltage of about 10 V/ $\mu$ m.

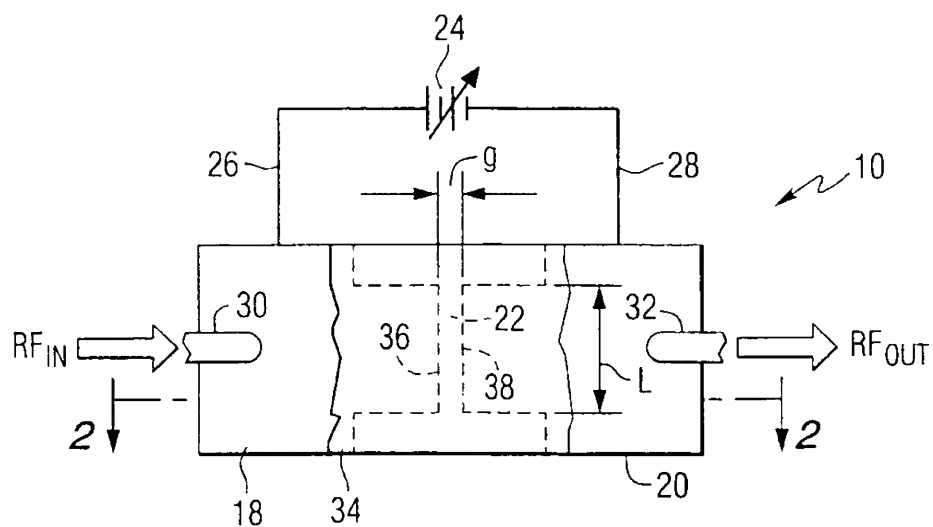
10 10. A tunable filter in accordance with claim 5, wherein the substrate comprises one of the group of: MgO, Alumina, LaAlO<sub>3</sub>, sapphire, and a ceramic.

11. A tunable filter in accordance with claim 5, wherein the tunable dielectric layer comprises one of:

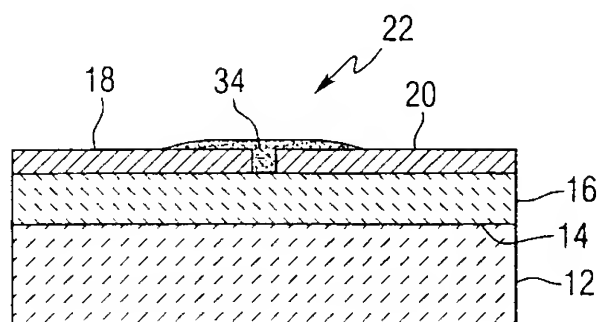
15 a tunable dielectric thick film;  
a tunable dielectric bulk ceramic; and  
a tunable dielectric thin film.

12. A tunable filter in accordance with claim 5, wherein the tunable dielectric includes an RF input and an RF output for passing an RF signal through the tunable dielectric layer in a first direction, and wherein the gap extends in a second  
20 direction substantially perpendicular to the first direction.

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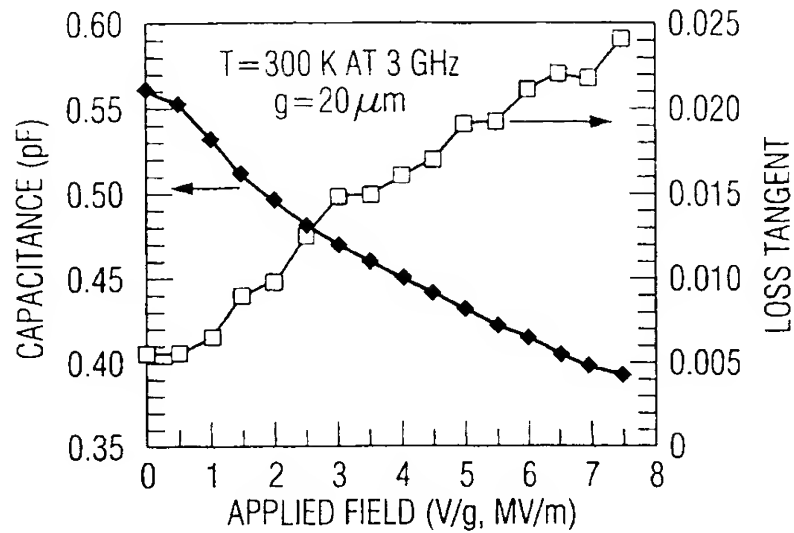
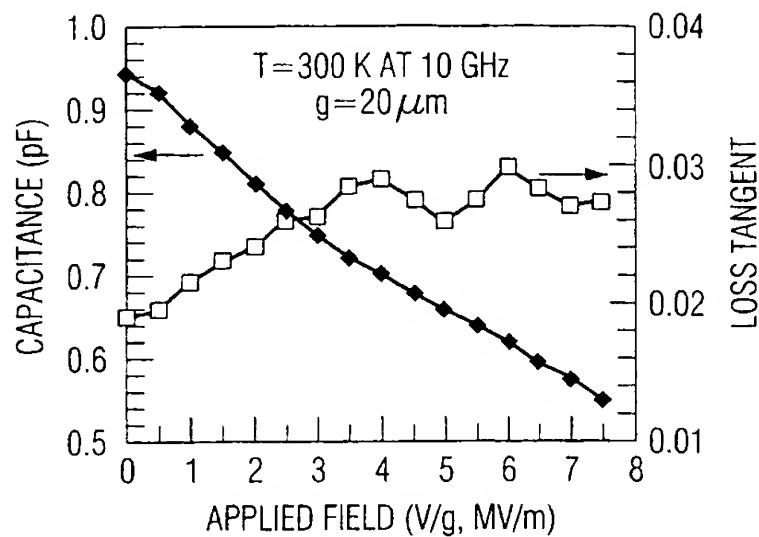
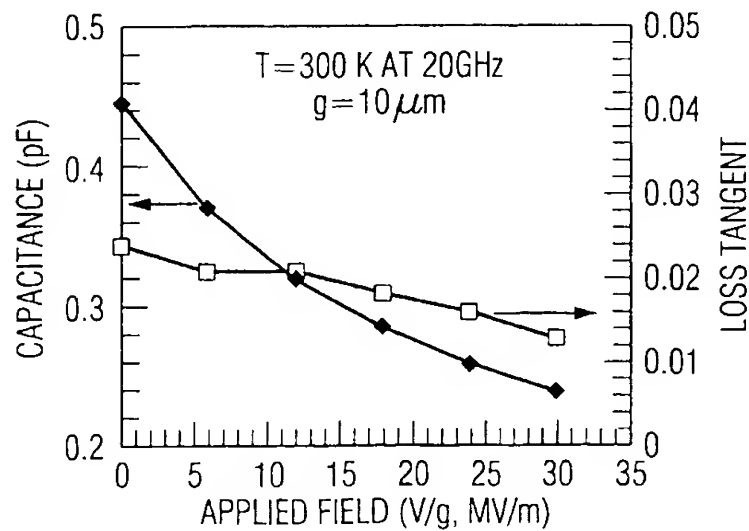


**FIG. 1**  
PRIOR ART

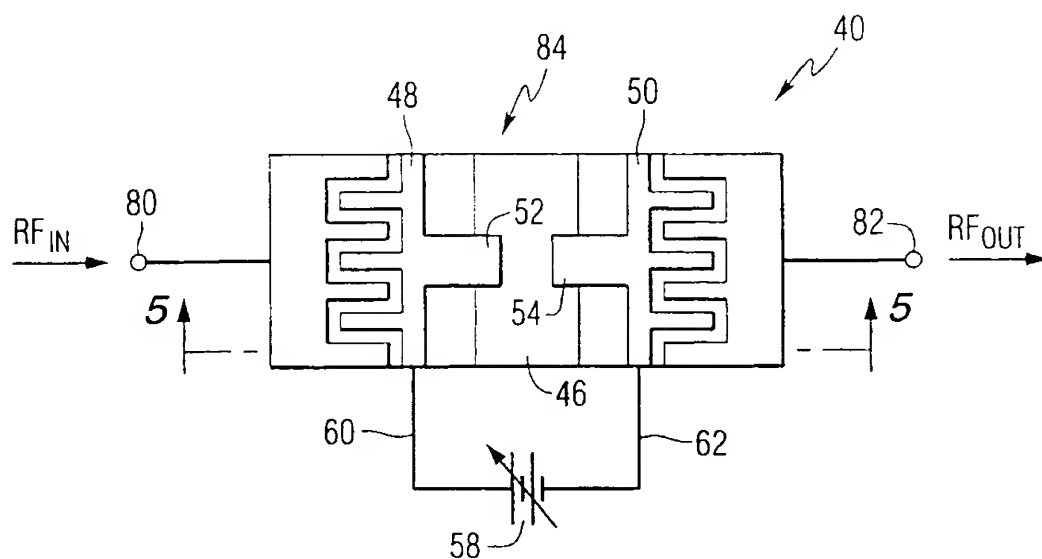


**FIG. 2**  
PRIOR ART

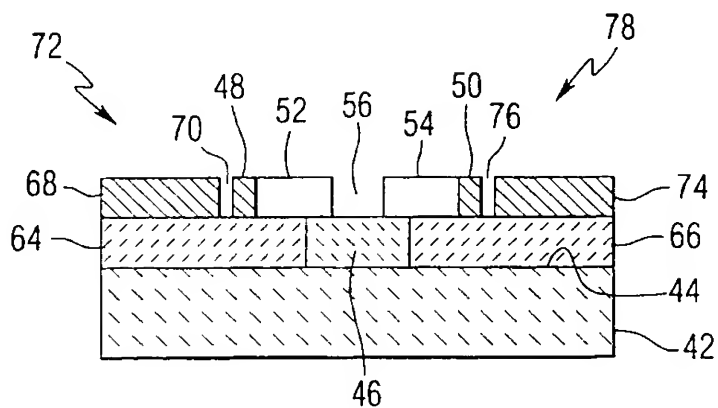
2/6

**FIG. 3a**  
PRIOR ART**FIG. 3b**  
PRIOR ART**FIG. 3c**  
PRIOR ART

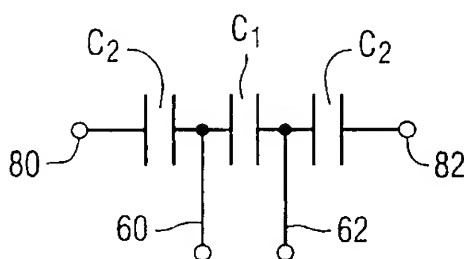
3/6



**FIG. 4**  
PRIOR ART

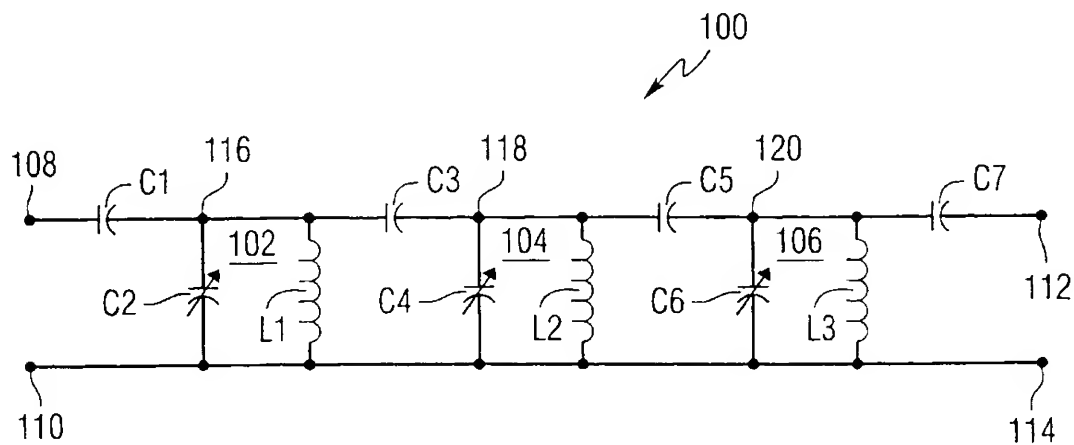
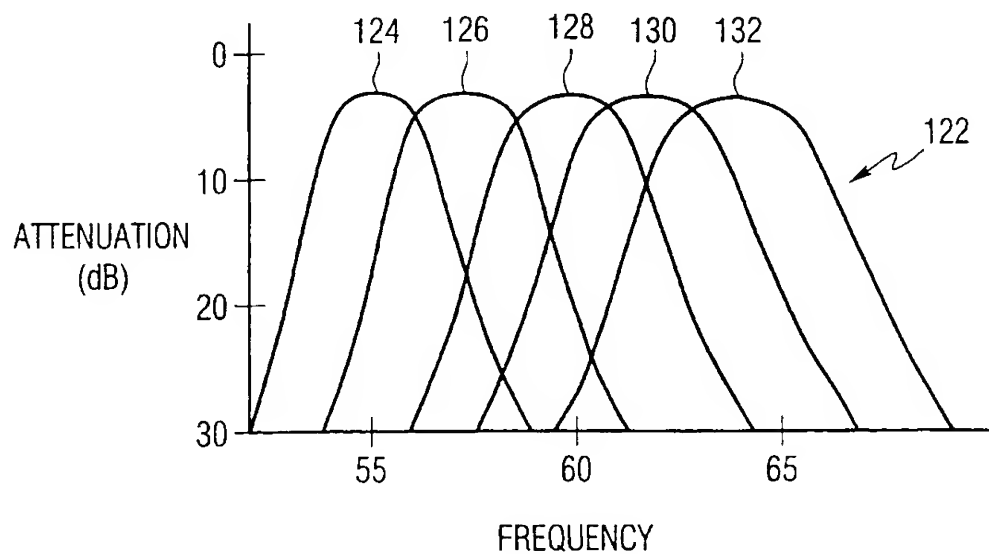


**FIG. 5**  
PRIOR ART



**FIG. 6**  
PRIOR ART

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**FIG. 7****FIG. 8**

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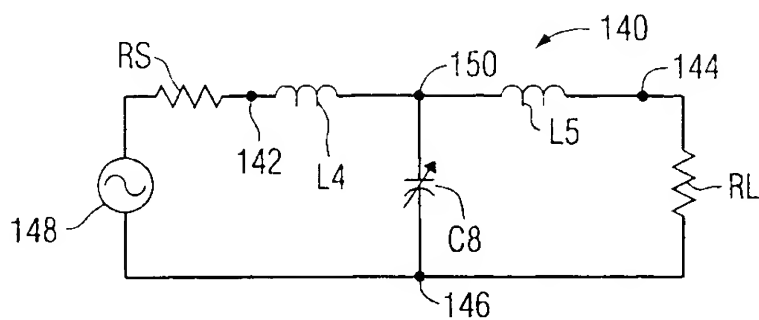


FIG. 9

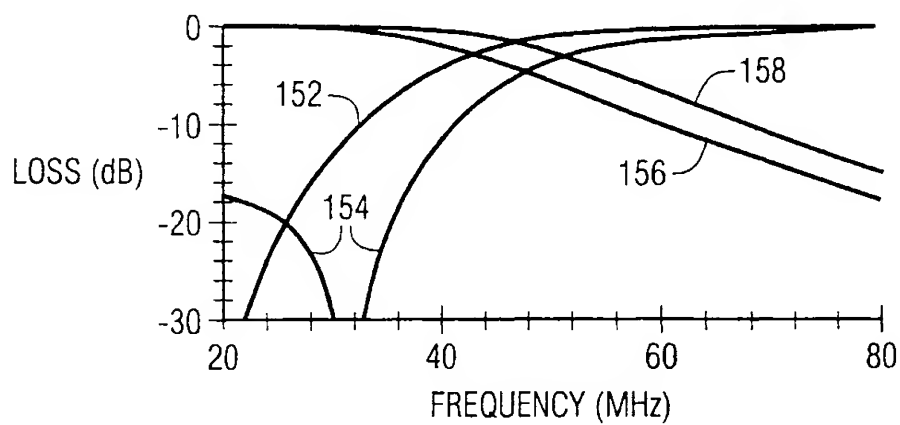


FIG. 10

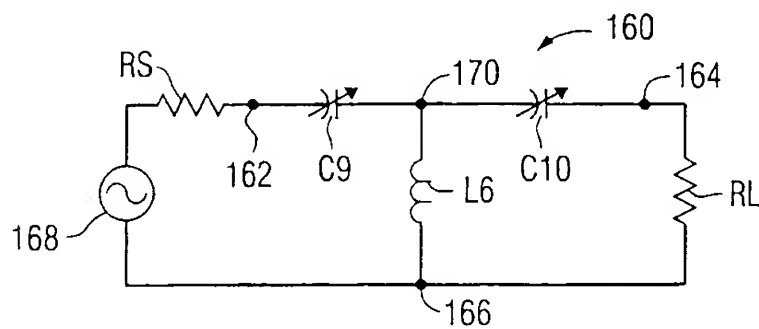


FIG. 11

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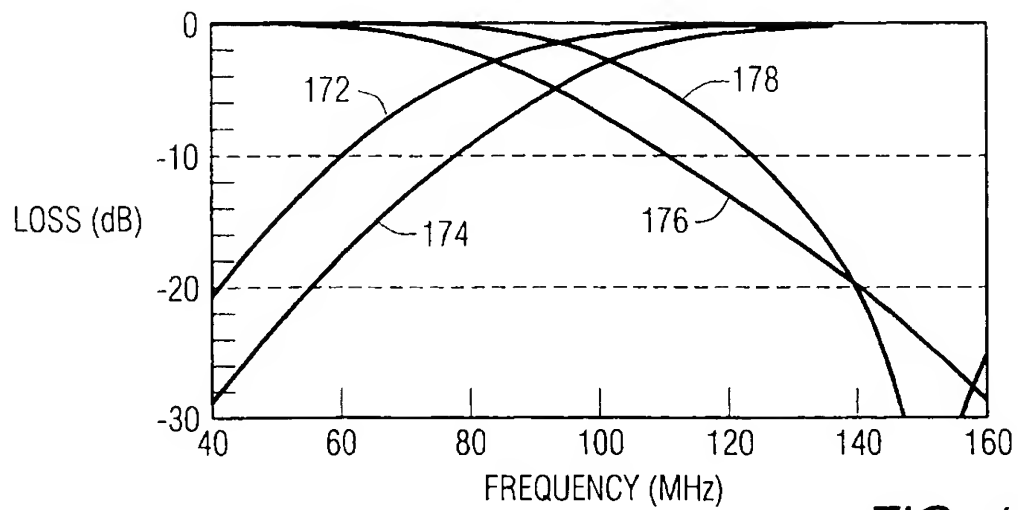


FIG. 12

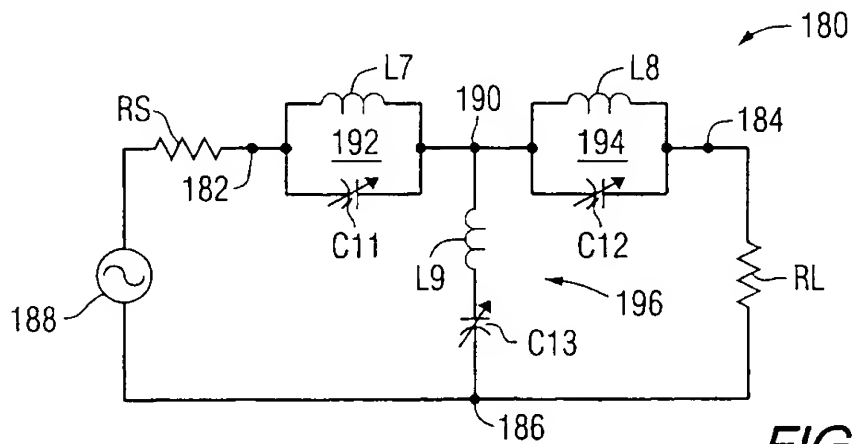


FIG. 13

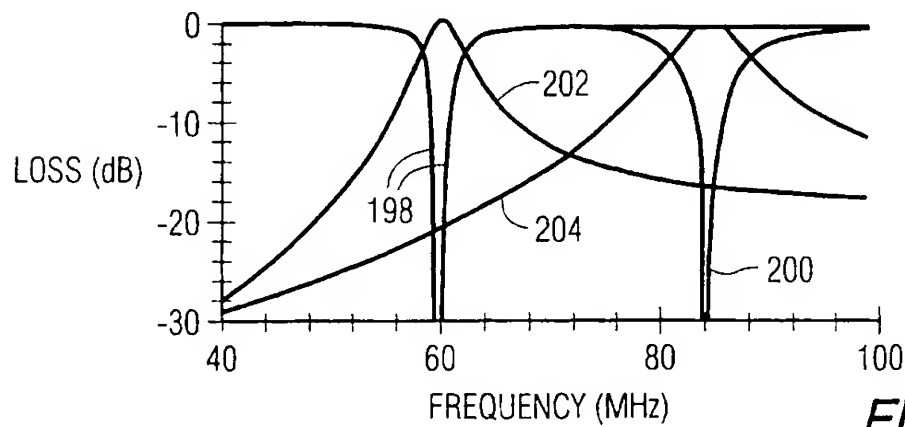


FIG. 14

# INTERNATIONAL SEARCH REPORT

International Application No.

PCT/US 99/29230

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 H01P1/20

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H03H H01G H01P

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 94 13028 A (SUPERCONDUCTING CORE TECHNOLOGIES, INC.) 9 June 1994 (1994-06-09) page 5, line 16 - line 25 page 7, line 8 - line 16 page 26, line 12 -page 27, line 2 page 32, line 12 -page 33, line 1; figures 20,21,29	1,2,4
Y		5,11,12
A		9



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

\* Special categories of cited documents:

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

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"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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"3" document member of the same patent family

Date of the actual completion of the international search

2 February 2000

Date of mailing of the international search report

10/02/2000

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Authorized officer

Den Otter, A



# INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 99/29230

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication where appropriate, of the relevant passages	Relevant to claim No
Y	<p>KOZYREV A ET AL: "FERROELECTRIC FILMS: NONLINEAR PROPERTIES AND APPLICATIONS IN MICROWAVE DEVICES"</p> <p>IEEE MTT-S INTERNATIONAL MICROWAVE SYMPOSIUM DIGEST, US, NEW YORK, NY: IEEE, 1998, pages 985-988, XP000822132 ISBN: 0-7803-4472-3</p> <p>figure 1</p>	5, 11, 12
A	<p>GEVORGIAN S S ET AL: "ELECTRICALLY CONTROLLED HTSC-FERROELECTRIC COPLANAR WAVEGUIDE"</p> <p>IEE PROCEEDINGS: MICROWAVES, ANTENNAS AND PROPAGATION, GB, IEE, STEVENAGE, HERTS, vol. 141, no. 6, PART H, 1 December 1994 (1994-12-01), pages 501-503, XP000484786</p> <p>ISSN: 1350-2417</p> <p>page 502, right-hand column, line 31 - line 35; figures 1, 2A</p>	7, 8, 10
A	<p>PATENT ABSTRACTS OF JAPAN</p> <p>vol. 1998, no. 2, 30 January 1998 (1998-01-30) &amp; JP 09 270338 A (TOKIN CORP.), 14 October 1997 (1997-10-14) abstract</p>	1
A	<p>US 5 496 796 A (SATYENDRANATH)</p> <p>5 March 1996 (1996-03-05)</p> <p>the whole document</p>	1

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 99/29230

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
WO 9413028	A	09-06-1994	US 5472935 A	05-12-1995
			AU 680866 B	14-08-1997
			AU 5897394 A	22-06-1994
			CA 2150690 A	09-06-1994
			EP 0672308 A	20-09-1995
			FI 953834 A	14-08-1995
			JP 8509103 T	24-09-1996
			US 5694134 A	02-12-1997
			US 5721194 A	24-02-1998
			US 5589845 A	31-12-1996
<hr/>				
JP 09270338	A	14-10-1997	NONE	
<hr/>				
US 5496796	A	05-03-1996	US 5900390 A	04-05-1999
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